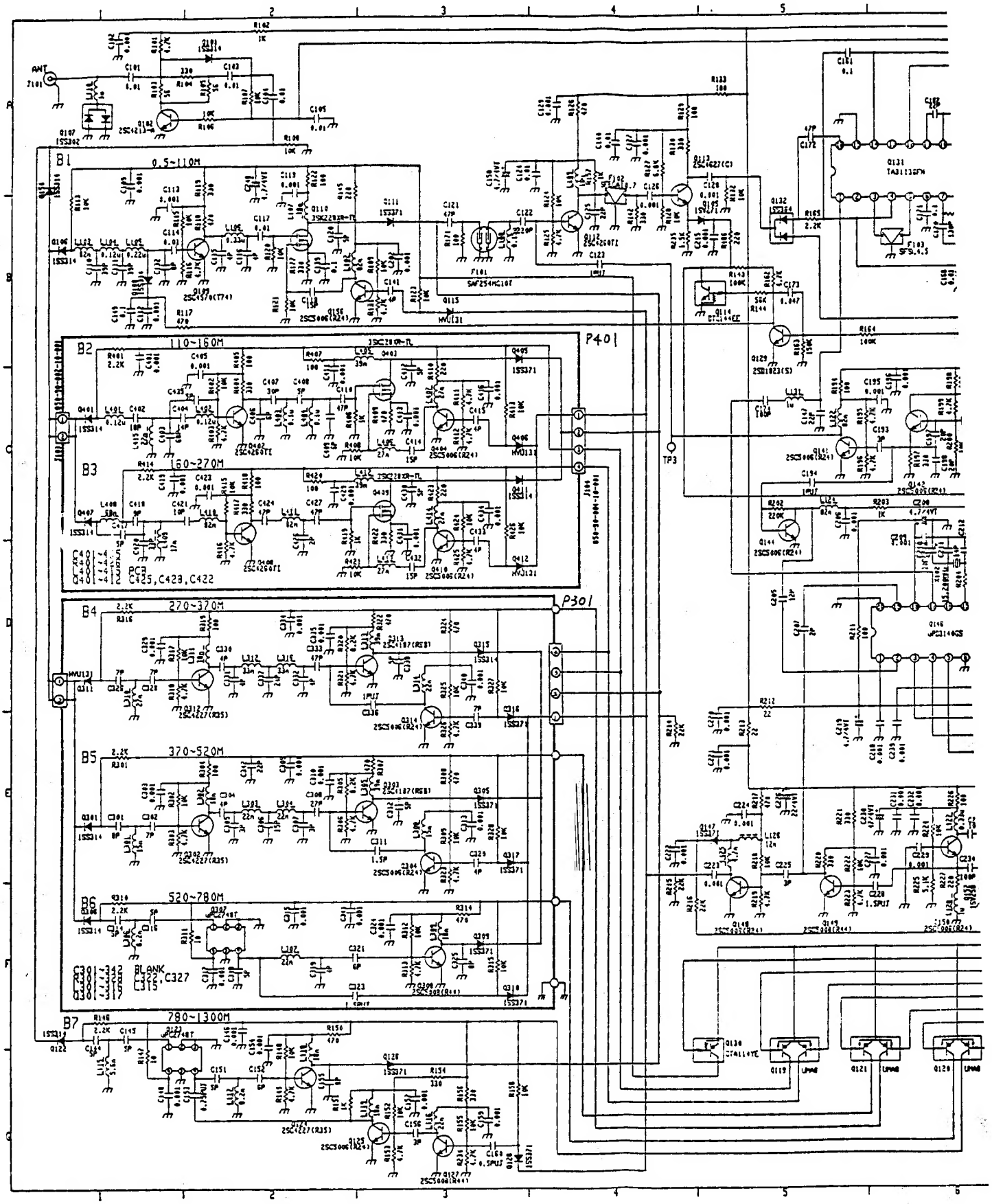
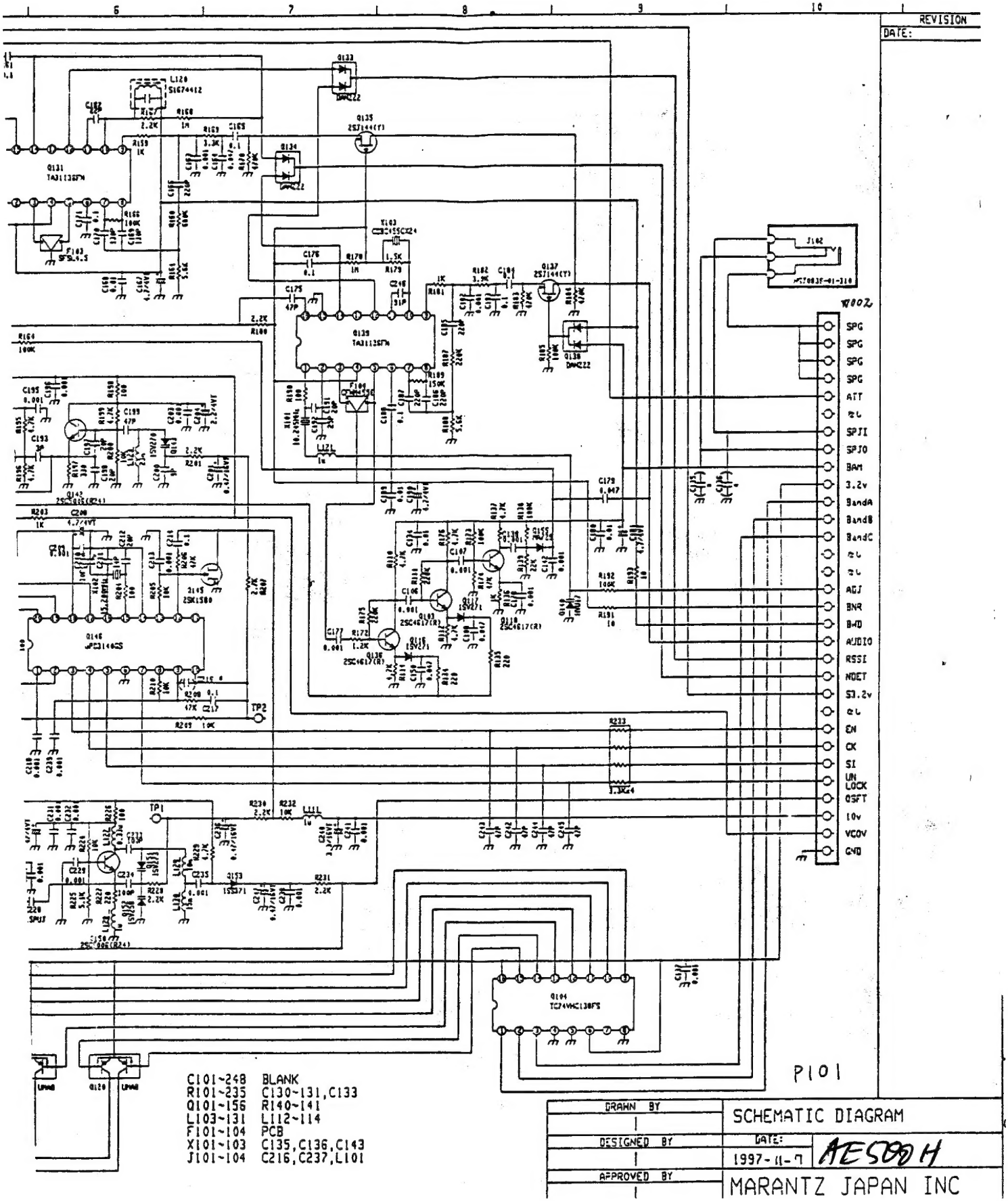
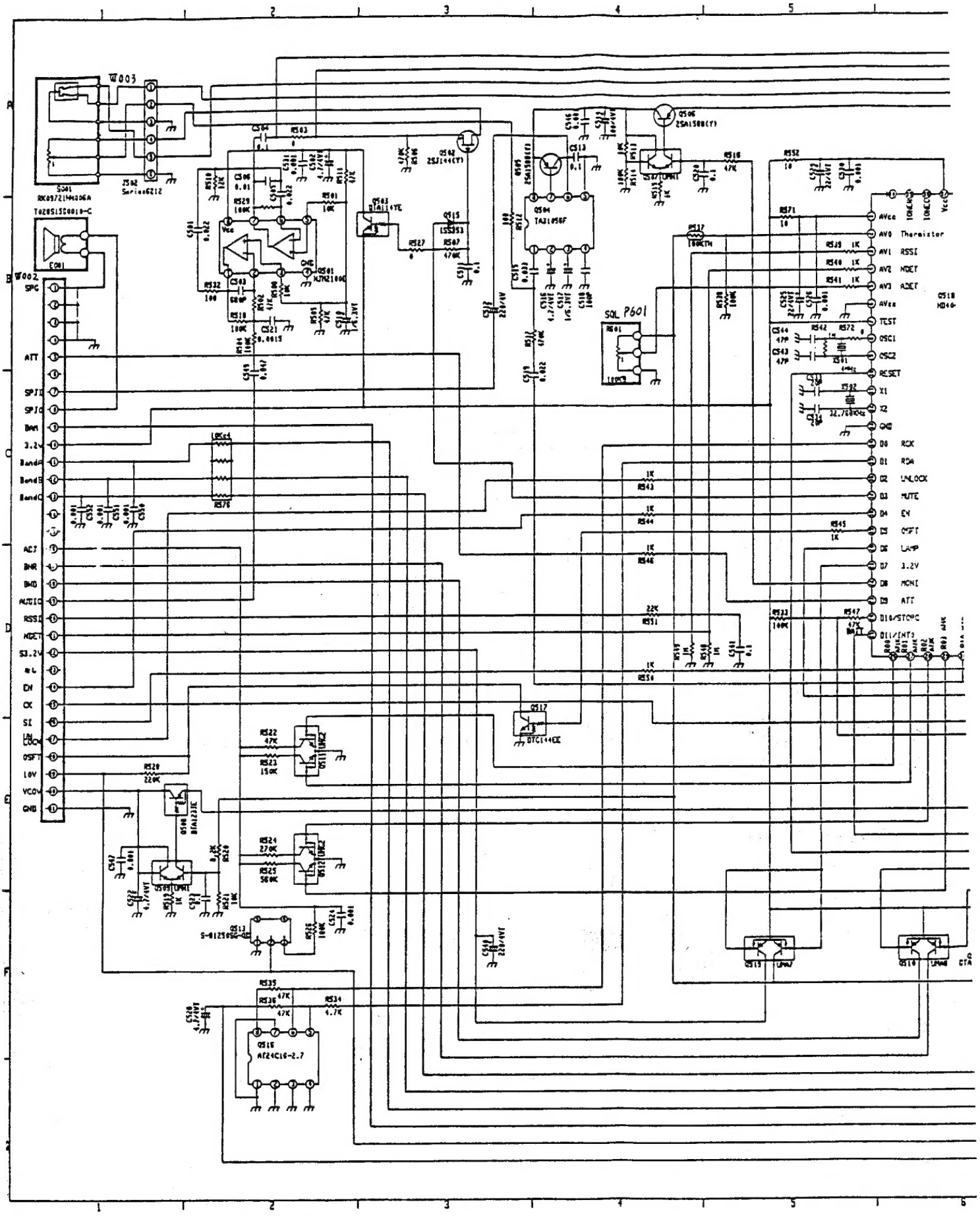
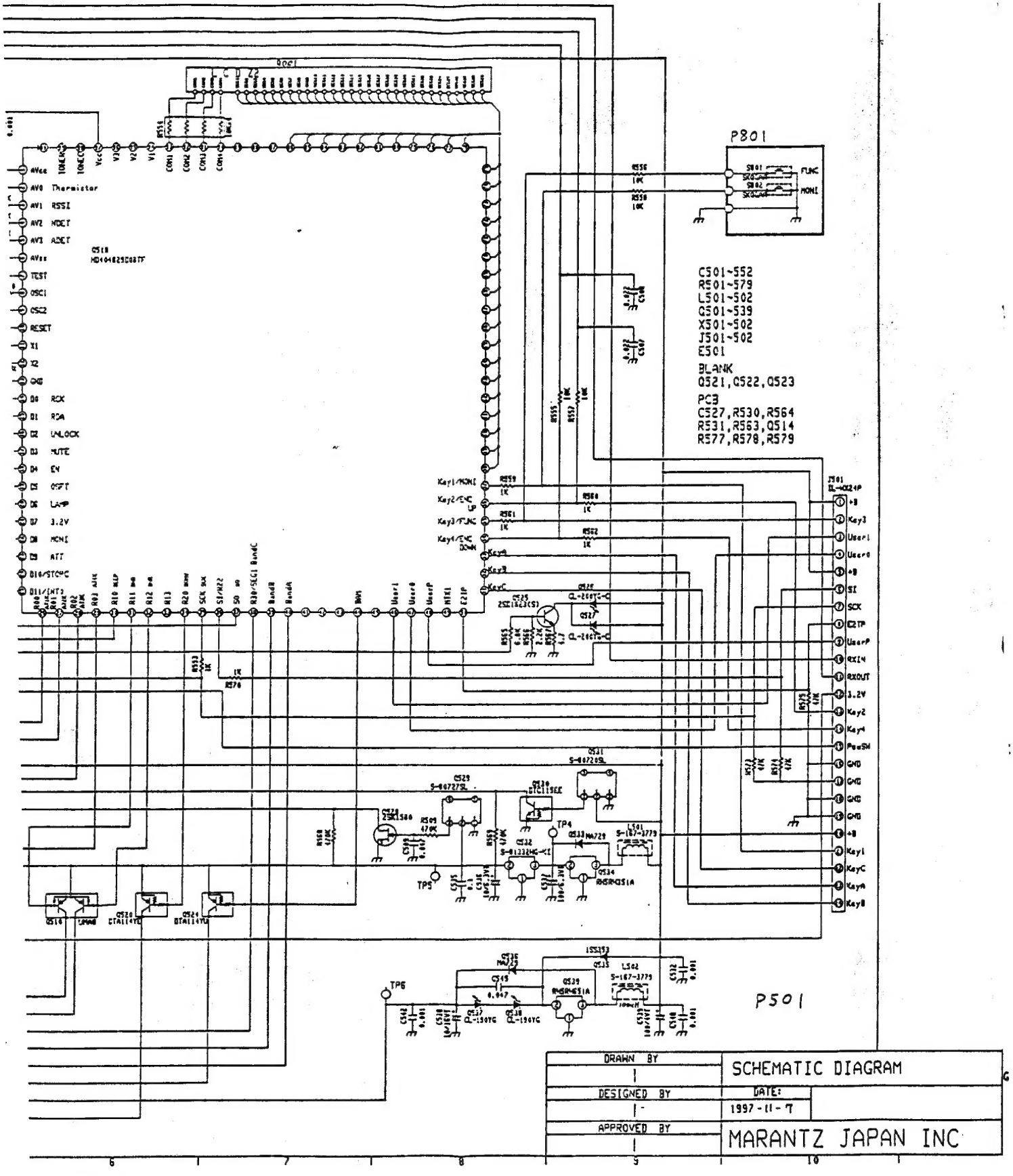

B1 0.5~110M
B2 110~160M
B3 160~270M
B4 270~370M
B5 370~520M
B6 520~780M
B7 780~1300M
P401
P301
TP3
ANT
BLANK
C425,C428,C422



C101~248 BLANK
R101~235 C130~131,C133
Q101~156 R140~141
L103~131 L112~114
F101~104 PCB
X101~103 C135,C136,C143
J101~104 C216,C237,L101

DRAWN BY	SCHEMATIC DIAGRAM	
DESIGNED BY	DATE: 1997-11-7	AES800H
APPROVED BY	MARANTZ JAPAN INC	


W003
W002
SPC
ATT
SPID
SPIC
Bm1
3.2V
BandA
BandB
BandC
AGT
BNR
BND
AUDIO
RSSI
MDET
S3.2V
EN
CK
SI
LOCK
OSFT
10V
VCOV
GND
SQL P601
AVcc
AV0 Thermistor
AV1 RSSI
AV2 MDET
AV3 ADET
TEST
OSC1
OSC2
RESET
X1
X2
D0 RCK
D1 RDA
D2 UNLOCK
D3 MUTE
D4 EN
D5 OSFT
D6 LAMP
D7 3.2V
D8 MONI
D9 ATT
D10/STOPC
D11/INT0
Q517
DTC144EE
Q515
Q516
AZ24C10-2.7


P801
P501
C501~552
R501~579
L501~502
Q501~539
X501~502
J501~502
E501
BLANK
Q521,Q522,Q523
PCB
C527,R530,R564
R531,R563,Q514
R577,R578,R579
DRAWN BY
DESIGNED BY
APPROVED BY
SCHEMATIC DIAGRAM
DATE:
1997-11-7
MARANTZ JAPAN INC